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Stiliadis, D.; Varma, A.;

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Yeon-Jae Jung; Seung-Wook Lee; Daeyun Shim; Wonchan Kim; Changhyun K Soo-In Cho;

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Zucker, D.F.; Lee, R.B.; Flynn, M.J.;

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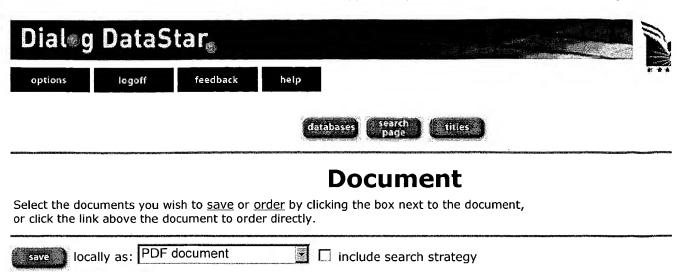
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Noro-R; Hamdi-M; Hubaux-J-P; Ed. by Touch-J-D; Sterbenz-J-P-G.

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Inst for Comput Commun & Applications, Swiss Fed Inst of Technol, Lausanne, Switzerland.

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Proceedings of Conference on Protocols for High Speed Networks, Salem, MA, USA, 25-27 Aug. 1999. In: p.187-201, 2000.

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Abstract

Recent developments in transmission and switching technology has made available low-cost gigabit **network** capacity for packet-based WAN (e.g., IP over WDM), that will not require the intermediate SONET/SDH multiplexing layer. For competitive operators that want to minimize their investment by deploying native IP over WDM networks, circuit **emulation** over IP gives support to the important class of legacy circuit-based equipment (e.g., T1/E1 multiplexers), which otherwise will require the deployment of an expensive telecommunication infrastructure, including SONET/SDH equipment. At the user interface, circuit **emulation** is required to maintain the same service of a circuit-based **network**, including synchronization and structured data delivery. In this paper we design the algorithm and the protocol that provide these two features. First, we identify the protocols and mechanisms used in the Internet and their ability to be used for circuit **emulation**. Second, we address the problem of synchronization and we propose an algorithm for efficient source **clock** recovery and handling of **delay** variations. Finally, we show through simulations that circuit **emulation** over IP provides the needed service performance at the user interface. (14 refs).

Descriptors

<u>Circuit-switching</u>; <u>packet-switching</u>; <u>protocols</u>; <u>synchronisation</u>; <u>wavelength-division-multiplexing</u>; <u>wide-area-networks</u>.

Keywords

circuit **emulation**; IP networks; packet based WAN; gigabit **network** capacity; WDM; legacy circuit based equipment; synchronization; structured data delivery; protocol; Internet; source **clock** recovery; **delay** variation; simulations; service performance; user interface.

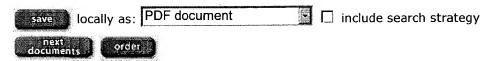
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ACCESSES	91204
CYCLE	607778
CYCLES	298482
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Search Results - Record(s) 1 through 7 of 7 returned.

☐ 1. Document ID: US 20040085994 A1

Using default format because multiple data bases are involved.

L26: Entry 1 of 7

File: PGPB

May 6, 2004

PGPUB-DOCUMENT-NUMBER: 20040085994

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20040085994 A1

TITLE: Methods and apparatus for device access fairness in fibre channel arbitrated

loop systems

PUBLICATION-DATE: May 6, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Warren, Bruce Gregory	Poulsbo	WA	US	
Goodwin, William	Bothell	WA	US	
Mies, Carl	Snohomish	WA	US	
Johnson, Bruce E.	Federal Way	WA	US	
White, Michael L.	Oak Harbor	WA	US	
Eng, Warren	Renton	WA	US	

US-CL-CURRENT: 370/462; 370/419

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draim De
	2.]	Docume	nt ID:	US 20	040085974	A1		anggag man kepanan kalanda dan kalancenda keranggan kenalanggan dan kelanggan kenalanggan kelanggan beranggan	***************************************	**************************************	· · · · · · · · · · · · · · · · · · ·	
L26:	Entry	y 2 of	7				File: F	GPB		May	6,	2004

PGPUB-DOCUMENT-NUMBER: 20040085974

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20040085974 A1

TITLE: Methods and apparațus for device zoning in fibre channel arbitrated loop

systems

PUBLICATION-DATE: May 6, 2004

INVENTOR-INFORMATION:

NAME CITY STATE COUNTRY RULE-47

Mies, Carl Snohomish WA US Warren, Bruce Gregory Poulsbo WA US US-CL-CURRENT: 370/406

Full Title Citation Front Review Classification Date Reference Sequences Attachments Claims KVMC Draw. De

☐ 3. Document ID: US 20040085972 A1

L26: Entry 3 of 7

File: PGPB

May 6, 2004

PGPUB-DOCUMENT-NUMBER: 20040085972

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20040085972 A1

TITLE: Methods and apparatus for trunking in fibre channel arbitrated loop systems

PUBLICATION-DATE: May 6, 2004

INVENTOR-INFORMATION:

STATE COUNTRY RULE-47 CITY NAME Poulsbo WA US Warren, Bruce Gregory Goodwin, William WA US Bothell WAUS Mies, Carl Snohomish US Everett WA Hammond-Doel, Thomas WA US Oak Harbor White, Michael L.

US-CL-CURRENT: 370/401

s KOMO Draw	Claims	Attachments	Sequences	Reference	Date	Classification	Review	Front	Citation	Title	Full

4. Document ID: US 20040081187 A1

L26: Entry 4 of 7

File: PGPB

Apr 29, 2004

PGPUB-DOCUMENT-NUMBER: 20040081187

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20040081187 A1

TITLE: Methods and apparatus for switching fibre channel arbitrated loop systems

PUBLICATION-DATE: April 29, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Warren, Bruce Gregory	Poulsbo	WA	US	
Goodman, William P.	Bothell	AW	US	
Mies, Carl	Snohomish	WA	US	
Johnson, Bruce E.	Federal Way	WA	US	
White, Michael L.	Oak Harbor	WA	US	
Eng, Warren	Renton	AW	US	

US-CL-CURRENT: 370/419

5. Document ID: US 20040081186 A1

L26: Entry 5 of 7

File: PGPB

Apr 29, 2004

PGPUB-DOCUMENT-NUMBER: 20040081186

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20040081186 A1

TITLE: Methods and apparatus for switching Fibre Channel Arbitrated Loop devices

PUBLICATION-DATE: April 29, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Warren, Bruce Gregory	Poulsbo	WA	US	
Goodwin, William	Bothell	WA	US	
Mies, Carl	Snohomish	WA	US	
White, Michael L.	Oak Harbor	WA	US	
Eng, Warren	Renton	WA	US	
Johnson, Bruce E.	Federal Way	WA	US	

US-CL-CURRENT: <u>370/419</u>

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draint D
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☑ 6. Document ID: US 6377561 B1

L26: Entry 6 of 7

File: USPT

Apr 23, 2002

US-PAT-NO: 6377561

DOCUMENT-IDENTIFIER: US 6377561 B1

TITLE: Data communication satellite system and method of carrying multi-media

traffic

DATE-ISSUED: April 23, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Black; Colin J.	St. Lazare			CA
Takats; Peter	Ile Perrot			CA
Cote; Martin	Montreal			CA
Le-Ngoc; Tho T.	Anjou			CA

US-CL-CURRENT: 370/330

Full	Title	Citation	Front	Review	Classification	Date	Reference	25 (200)	Claims	KWIC	Draw, D
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7. Document ID: US 4486827 A

L26: Entry 7 of 7

File: USPT

Dec 4, 1984

Record List Display

US-PAT-NO: 4486827

DOCUMENT-IDENTIFIER: US 4486827 A

** See image for Certificate of Correction **

TITLE: Microprocessor apparatus

DATE-ISSUED: December 4, 1984

INVENTOR-INFORMATION:

NAME

CITY

STATE

ZIP CODE

COUNTRY

Shima; Masatoshi

Santa Clara

CA CA

Faggin; Federico

Ungermann; Ralph K.

Cupertino Los Altos

CA

US-CL-CURRENT: 710/261

ull Title Citation Front Review Classification Date Reference	Saucenses Alas Emerica Claims KMC
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Term	Documents
CYCLE	607778
CYCLES	298482
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Search Results - Record(s) 1 through 8 of 8 returned.

☐ 1. Document ID: US 5426765 A

Using default format because multiple data bases are involved.

L16: Entry 1 of 8

File: USPT

Jun 20, 1995

US-PAT-NO: 5426765

DOCUMENT-IDENTIFIER: US 5426765 A

TITLE: Multiprocessor cache abitration

DATE-ISSUED: June 20, 1995

INVENTOR-INFORMATION:

NAME CITY STATE ZIP CODE COUNTRY Stevens; Jeffrey C. Spring ΤX Jackson; Mike T. Houston TXTipley; Roger E. Houston TXRamsey; Jens K. Houston TXOlarig; Sompong Cypress TXKelly; Philip C. Houston TX

US-CL-CURRENT: 711/131; 711/146, 711/167

Full Titl	e Unation	Front	Review	Classification	Date	Reference	radioace (California) de	Claims	KWIC	Drawii D
								Ø18111151	10000	D1200 C

☐ 2. Document ID: US 5097437 A

L16: Entry 2 of 8

File: USPT

Mar 17, 1992

US-PAT-NO: 5097437

DOCUMENT-IDENTIFIER: US 5097437 A

TITLE: Controller with clocking device controlling first and second state machine controller which generate different control signals for different set of devices

DATE-ISSUED: March 17, 1992

INVENTOR-INFORMATION:

NAME CITY

0111

STATE ZIP CODE

COUNTRY

Larson; Ronald J.

Minneapolis

MN

55419

US-CL-CURRENT: <u>712/245</u>; <u>703/25</u>

☐ 3. Document ID: US 5053947 A

L16: Entry 3 of 8

File: USPT

Oct 1, 1991

US-PAT-NO: 5053947

DOCUMENT-IDENTIFIER: US 5053947 A

TITLE: Extended multistation bus system and method

DATE-ISSUED: October 1, 1991

INVENTOR-INFORMATION:

NAME

CITY

STATE

ZIP CODE

COUNTRY

Heibel; John T.

Columbus

OH

Savarda; Raymond R.

Centerburg

OH

US-CL-CURRENT: 709/237; 370/389, 370/438

☐ 4. Document ID: US 4137565 A

L16: Entry 4 of 8

File: USPT

Jan 30, 1979

US-PAT-NO: 4137565

DOCUMENT-IDENTIFIER: US 4137565 A

TITLE: Direct memory access module for a controller

DATE-ISSUED: January 30, 1979

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Mager; George E.	Manhattan Beach	CA		
Nelson; Frank M.	Sherman Oaks	CA		
Gillett; Kenneth	Redondo Beach	CA		
Holt; Charles P.	Palos Verdes	CA		
Steiner; Edward L.	Macedon	NY		
Daughton; John W.	Fairport	NY		
Fiske; Kenton W.	Palos Verdes	CA		
Criswell; Thomas	Venice	CA		
Hall; Warren L.	Fairport	NY		

US-CL-CURRENT: 710/22

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	•									

☐ 5. Document ID: US 4131945 A

L16: Entry 5 of 8

File: USPT

Dec 26, 1978

Record List Display

US-PAT-NO: 4131945

DOCUMENT-IDENTIFIER: US 4131945 A

TITLE: Watch dog timer module for a controller

DATE-ISSUED: December 26, 1978

INVENTOR-INFORMATION:

NAME CITY STATE ZIP CODE COUNTRY

Richardson; Philip Pacific Palisades CA Steiner; Edward L. Macedon NY Daughton; John W. Fairport NY

Fiske; Kenton W. Fairport NY Criswell; Thomas Venice CA

US-CL-CURRENT: 714/55



☐ 6. Document ID: US 4131944 A

L16: Entry 6 of 8

File: USPT

Dec 26, 1978

US-PAT-NO: 4131944

DOCUMENT-IDENTIFIER: US 4131944 A

TITLE: System bus module for a controller

DATE-ISSUED: December 26, 1978

INVENTOR-INFORMATION:

NAME CITY STATE ZIP CODE COUNTRY

Mager; George E. Manhattan Beach CA
Nelson; Frank M. Sherman Oaks CA
Hall; Warren L. Palos Verdes CA

US-CL-CURRENT: 710/107

Full Title Citation Front Review Classification Date Reference (1995) 1995 (1995) Claims KMC Draw De

☐ 7. Document ID: US 4131942 A

L16: Entry 7 of 8

File: USPT

Dec 26, 1978

US-PAT-NO: 4131942

DOCUMENT-IDENTIFIER: US 4131942 A

TITLE: Non-volatile storage module for a controller

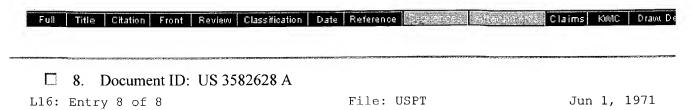
DATE-ISSUED: December 26, 1978

INVENTOR-INFORMATION:

Record List Display

COUNTRY NAME CITY STATE ZIP CODE Gillett; Kenneth Redondo Beach CA NY Steiner; Edward L. Macedon NY Fiske; Kenton W. Fairport NY Davis; Kenneth A. Honeoye Falls Kukucka; William P. NY Webster Criswell; Thomas Venice CACA Richardson; Philip Pacific Palisades

US-CL-CURRENT: 714/22



US-PAT-NO: 3582628

DOCUMENT-IDENTIFIER: US 3582628 A

TITLE: ANALOG-DIGITAL COMPUTER INTERCONNECTION SYSTEM

DATE-ISSUED: June 1, 1971

INVENTOR-INFORMATION:

NAME

CITY

STATE

ZIP CODE

COUNTRY

Brussolo; John A.

Ann Arbor

MΙ

US-CL-CURRENT: 708/2; 708/6, 708/817, 708/835, 708/840

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Search Results - Record(s) 1 through 4 of 4 returned.

☐ 1. Document ID: US 4137565 A

Using default format because multiple data bases are involved.

L13: Entry 1 of 4

File: USPT

Jan 30, 1979

US-PAT-NO: 4137565

DOCUMENT-IDENTIFIER: US 4137565 A

TITLE: Direct memory access module for a controller

DATE-ISSUED: January 30, 1979

INVENTOR-INFORMATION:

COUNTRY STATE ZIP CODE NAME CITY Mager; George E. Manhattan Beach CA CA Nelson; Frank M. Sherman Oaks Gillett; Kenneth Redondo Beach CA Holt; Charles P. Palos Verdes CA NY Steiner; Edward L. Macedon Daughton; John W. Fairport NY CA Fiske; Kenton W. Palos Verdes Criswell; Thomas Venice CA Hall; Warren L. Fairport NY

US-CL-CURRENT: 710/22

Title	Citation	Front	Review	Classification	Date	Reference	Service of		Claims	KWIC	Drawu
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☐ 2. Document ID: US 4131945 A

L13: Entry 2 of 4

File: USPT

Dec 26, 1978

US-PAT-NO: 4131945

DOCUMENT-IDENTIFIER: US 4131945 A

TITLE: Watch dog timer module for a controller

DATE-ISSUED: December 26, 1978

INVENTOR-INFORMATION:

NAME CITY STATE ZIP CODE COUNTRY

Richardson; Philip Pacific Palisades CA
Steiner; Edward L. Macedon NY
Daughton; John W. Fairport NY

http://westbrs:9000/bin/gate.exe?f=TOC&state=9368eq.14&ref=13&dbname=PGPB,USPT&... 9/1/04

Record List Display

Fiske; Kenton W.

Fairport

NY

Criswell; Thomas

Venice

CA

US-CL-CURRENT: 714/55



☐ 3. Document ID: US 4131944 A

L13: Entry 3 of 4

File: USPT

Dec 26, 1978

US-PAT-NO: 4131944

DOCUMENT-IDENTIFIER: US 4131944 A

TITLE: System bus module for a controller

DATE-ISSUED: December 26, 1978

INVENTOR-INFORMATION:

NAME

CITY

STATE ZIP CODE

COUNTRY

Dec 26, 1978

Mager; George E.

Manhattan Beach

CA

Nelson; Frank M.

Sherman Oaks

CA

Hall; Warren L.

Palos Verdes

CA

US-CL-CURRENT: 710/107

Full Title	Citation	Front	Review	Classification	Date	Reference	lite.	Claims	KWIC	Drawu D
	m nos									
CHARLES THE STREET OF THE STRE		******************		native programme and the second secon	***************************************	***************************************		************************		orecomments of

File: USPT

US-PAT-NO: 4131942

L13: Entry 4 of 4

DOCUMENT-IDENTIFIER: US 4131942 A

TITLE: Non-volatile storage module for a controller

DATE-ISSUED: December 26, 1978

INVENTOR-INFORMATION:

NAME COUNTRY CITY STATE ZIP CODE Gillett; Kenneth Redondo Beach CA Steiner; Edward L. Macedon NY Fiske; Kenton W. Fairport NY Davis; Kenneth A. Honeoye Falls NY Kukucka; William P. Webster NY Criswell; Thomas Venice CA Richardson; Philip Pacific Palisades CA

US-CL-CURRENT: <u>714/22</u>

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Search Results - Record(s) 1 through 17 of 17 returned.

☑ 1. Document ID: US 20020178427 A1

Using default format because multiple data bases are involved.

L33: Entry 1 of 17

File: PGPB

Nov 28, 2002

PGPUB-DOCUMENT-NUMBER: 20020178427

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20020178427 A1

TITLE: Method for improving timing behavior in a hardware logic emulation system

PUBLICATION-DATE: November 28, 2002

INVENTOR-INFORMATION:

COUNTRY RULE-47 STATE NAME CITY CA US Ding, Cheng-Liang Cupertino US Sunnyvale CA Freeman, Thomas H. Cupertino CA US Chao, Liang-Fang Ben-Tzur, Tzvi Sunnyvale CA US

US-CL-CURRENT: 716/12

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Drawe D
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☐ 2. Document ID: US 20020133325 A1

L33: Entry 2 of 17

File: PGPB

Sep 19, 2002

PGPUB-DOCUMENT-NUMBER: 20020133325

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20020133325 A1

TITLE: Discrete event simulator

PUBLICATION-DATE: September 19, 2002

INVENTOR-INFORMATION:

RULE-47 COUNTRY NAME CITY STATE Hoare, Raymond R. II Pittsburgh PAUS Ahn, Jiyong Pittsburgh PΑ US Graves, Jesse Pittsburgh PΑ US

US-CL-CURRENT: 703/17

Full Title Citation Front Review Classification Date Reference Sequences Attachments Claims KMC Draw De

☑ 3. Document ID: US 20010036188 A1

L33: Entry 3 of 17

File: PGPB

Nov 1, 2001

PGPUB-DOCUMENT-NUMBER: 20010036188

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20010036188 A1

TITLE: Multi-service circuit for telecommuncations

PUBLICATION-DATE: November 1, 2001

INVENTOR-INFORMATION:

NAME CITY STATE COUNTRY RULE-47

Carlsson, Dan Hasselby SE

US-CL-CURRENT: 370/395.4; 370/465, 370/535

Full Title Citation Front Review Classification Date Reference Sequences Attachments Claims KMC Draw. De

4. Document ID: US 6377561 B1

L33: Entry 4 of 17

File: USPT

Apr 23, 2002

US-PAT-NO: 6377561

DOCUMENT-IDENTIFIER: US 6377561 B1

TITLE: Data communication satellite system and method of carrying multi-media

traffic

DATE-ISSUED: April 23, 2002

INVENTOR-INFORMATION:

NAME CITY STATE ZIP CODE COUNTRY

Black; Colin J. St. Lazare CA
Takats; Peter Ile Perrot CA
Cote; Martin Montreal CA
Le-Ngoc; Tho T. Anjou CA

US-CL-CURRENT: 370/330

Full | Title | Citation | Front | Review | Classification | Date | Reference | Structure | Structure | Claims | KMC | Draw, Dr

5. Document ID: US 6256308 B1

L33: Entry 5 of 17

File: USPT

Jul 3, 2001

US-PAT-NO: 6256308

DOCUMENT-IDENTIFIER: US 6256308 B1

Record List Display

TITLE: Multi-service circuit for telecommunications

DATE-ISSUED: July 3, 2001

INVENTOR-INFORMATION:

NAME

CITY

ZIP CODE STATE

File: USPT

COUNTRY

Carlsson; Dan

Hasselby

SE

US-CL-CURRENT: 370/395.43; 370/395.5, 370/465, 370/535

Full Title Citation Front Review Classification Date Reference Sergiences Acochimens Claims KMC Draw. De ☐ 6. Document ID: US 5893049 A

US-PAT-NO: 5893049

L33: Entry 6 of 17

DOCUMENT-IDENTIFIER: US 5893049 A

TITLE: Rapid response voltage threshold determination circuit for electrophysiology

diagnostic device

DATE-ISSUED: April 6, 1999

INVENTOR-INFORMATION:

NAME

CITY

ZIP CODE STATE

COUNTRY

Apr 6, 1999

Reggiardo; Christopher V.

Alviso CA

US-CL-CURRENT: 702/71; 702/193, 714/735, 714/736

Full	Title	Citation	Front	Review	Classification	Date	Reference	Maria partiane	Claims	KMC	Draw. D
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	7.	Documer	nt ID:	US 58	35751 A						
		y 7 of :					File: U			10	1998

US-PAT-NO: 5835751

DOCUMENT-IDENTIFIER: US 5835751 A

TITLE: Structure and method for providing reconfigurable emulation circuit

DATE-ISSUED: November 10, 1998

INVENTOR-INFORMATION:

NAME CITY STATE ZIP CODE COUNTRY Chen; Nang-Ping Cupertino CA Ko; Robert J. CA Saratoga Li; Jeong-Tyng CA Cupertino Huang; Thomas B. San Jose CA Wang; Ming-Yang Lafayette CA

US-CL-CURRENT: 716/16; 327/39



□ 8. Document ID: US 5793356 A

L33: Entry 8 of 17

File: USPT

Aug 11, 1998

US-PAT-NO: 5793356

DOCUMENT-IDENTIFIER: US 5793356 A

TITLE: System and method for the software emulation of a computer joystick

DATE-ISSUED: August 11, 1998

INVENTOR-INFORMATION:

STATE ZIP CODE COUNTRY NAME CITY

Kanagawa-ken

Svancarek; Mark K. Redmond WA Woodinville AWAdan; Manolito E. WAVan Flandern; Michael W. Redmond

Pierce; John G. Vashon WA

Suzuki; Hajime Kash; Richard B. Los Altos Hills CA

US-CL-CURRENT: 345/161; 345/157, 463/38



☐ 9. Document ID: US 5754828 A

L33: Entry 9 of 17

File: USPT

May 19, 1998

JΡ

US-PAT-NO: 5754828

DOCUMENT-IDENTIFIER: US 5754828 A

** See image for Certificate of Correction **

TITLE: System and method for positional control device data source selection

DATE-ISSUED: May 19, 1998

INVENTOR-INFORMATION:

COUNTRY NAME CITY STATE ZIP CODE

Adan; Manolito E. Woodinville WA Van Flandern; Michael W. Redmond WA Dean; Daniel Issaquah WA Davis; Jeffrey A. Bothell WA

US-CL-CURRENT: 703/24



☐ 10. Document ID: US 5724558 A

Page 5 of 8

Record List Display

L33: Entry 10 of 17

File: USPT

Mar 3, 1998

US-PAT-NO: 5724558

DOCUMENT-IDENTIFIER: US 5724558 A

TITLE: System and method for dynamic data packet configuration

DATE-ISSUED: March 3, 1998

INVENTOR-INFORMATION:

NAME

STATE CITY WA

ZIP CODE COUNTRY

Svancarek; Mark K. Adan; Manolito E.

Woodinville

WA

Van Flandern; Michael W.

Redmond

Redmond

WA

Suzuki; Hajime

Kanagawa-ken

JΡ

US-CL-CURRENT: 703/21; 345/156, 345/161, 345/168, 703/24, 710/20, 710/58, 712/227, 719/321

Full Title Citation Front Review Classification Date Reference Section 2 Attachmining Claims KWIC Draw. De ☐ 11. Document ID: US 5716381 A Feb 10, 1998

File: USPT

US-PAT-NO: 5716381

L33: Entry 11 of 17

DOCUMENT-IDENTIFIER: US 5716381 A

TITLE: Electrophysiology diagnostic device including variable capacitance emulation

and voltage threshold determination circuits

DATE-ISSUED: February 10, 1998

INVENTOR-INFORMATION:

NAME

CITY

STATE ZIP CODE COUNTRY

Reggiardo; Christopher V.

Alviso

US-CL-CURRENT: 607/8; 607/5, 607/7

Full Title Citation Front Review Classification Date Reference Reference Authorities Associated Claims KWIC Draw De

☐ 12. Document ID: US 5694949 A

L33: Entry 12 of 17

File: USPT

Dec 9, 1997

US-PAT-NO: 5694949

DOCUMENT-IDENTIFIER: US 5694949 A

TITLE: Variable capacitance emulation circuit for electrophysiology diagnostic

device

DATE-ISSUED: December 9, 1997

Record List Display

INVENTOR-INFORMATION:

NAME

CITY

STATE ZIP CODE

COUNTRY

Reggiardo; Christopher V.

Alviso CA

US-CL-CURRENT: 128/897; 323/354

Full Title Citation Front Review Classification Date Reference G. P. C. Alta Michigan & Claims KMC Draw De

☐ 13. Document ID: US 5682353 A

L33: Entry 13 of 17

File: USPT

Oct 28, 1997

US-PAT-NO: 5682353

DOCUMENT-IDENTIFIER: US 5682353 A

TITLE: Self adjusting sense amplifier clock delay circuit

DATE-ISSUED: October 28, 1997

INVENTOR-INFORMATION:

NAME

CITY

STATE ZIP CODE

ZIP CODE

COUNTRY

Eitan; Boaz

Ra'anana

TT.

Petersen; Larry Willis

Pocatello

ID

Slezak; Yaron

Kiriat Motzkin

IL

US-CL-CURRENT: 365/233; 365/185.21, 365/194, 365/233.5

Full Title Citation Front Review Classification Date Reference Colored Alachimotors Claims KMC Draw De

☐ 14. Document ID: US 5649167 A

L33: Entry 14 of 17

File: USPT

Jul 15, 1997

US-PAT-NO: 5649167

DOCUMENT-IDENTIFIER: US 5649167 A

** See image for Certificate of Correction **

TITLE: Methods for controlling timing in a logic emulation system

DATE-ISSUED: July 15, 1997

INVENTOR-INFORMATION:

NAME STATE CITY Chen; Nang-Ping Cupertino CA Ko; Robert J. Saratoga CA Li; Jeong-Tyng CA Cupertino Huang; Thomas B. San Jose CA

Wang; Ming-Yang

Lafayette

CA

US-CL-CURRENT: 703/23

Full Title Citation Front Review Classification Date Reference Servicing M. Research Claims KWIC Draw, De

COUNTRY

☐ 15. Document ID: US 5628686 A

L33: Entry 15 of 17

File: USPT

May 13, 1997

US-PAT-NO: 5628686

DOCUMENT-IDENTIFIER: US 5628686 A

TITLE: Apparatus and method for bidirectional data communication in a game port

DATE-ISSUED: May 13, 1997

INVENTOR-INFORMATION:

NAME CITY

ITY STATE ZIP CODE COUNTRY

Svancarek; Mark K. Redmond WA Adan; Manolito E. Woodinville WA

Van Flandern; Michael W. Redmond WA

Suzuki; Hajime Kanagawa-ken JP

US-CL-CURRENT: <u>463/36</u>

Full Title Citation Front Review Classification Date Reference N. 1995 (1995) Claims KMC Draw. De

☐ 16. Document ID: US 5475830 A

L33: Entry 16 of 17

File: USPT

Dec 12, 1995

US-PAT-NO: 5475830

DOCUMENT-IDENTIFIER: US 5475830 A

TITLE: Structure and method for providing a reconfigurable emulation circuit

without hold time violations

DATE-ISSUED: December 12, 1995

INVENTOR-INFORMATION:

NAME CITY STATE ZIP CODE COUNTRY

Chen; Nang-Ping Cupertino CA

Ko; Robert J. Saratoga CA

Li; Jeong-Tyng Cupertino CA

Huang; Thomas B. San Jose CA

Wang; Ming-Yang Lafayette CA

US-CL-CURRENT: <u>716/16</u>; <u>716/17</u>, <u>716/7</u>

Full | Title | Citation | Front | Review | Classification | Date | Reference | Secondary | Attachinguistic | Claims | KWC | Draw, De

☐ 17. Document ID: US 5452239 A

L33: Entry 17 of 17

File: USPT

Sep 19, 1995

US-PAT-NO: 5452239

Record List Display

DOCUMENT-IDENTIFIER: US 5452239 A

TITLE: Method of removing gated <u>clocks from the clock</u> nets of a netlist for timing sensitive implementation of the netlist in a hardware emulation system

DATE-ISSUED: September 19, 1995

INVENTOR-INFORMATION:

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US-CL-CURRENT: 703/19; 714/725, 716/17

Title Citation Front Review Classification Date Reference	coupy
Generate Collection Print Fwd Refs	Bkwd Refs Generate OAC
Term	Documents
PROGRAMMABLE	168888
PROGRAMMABLES	10
(32 AND PROGRAMMABLE).PGPB,USPT.	17
(L32 AND PROGRAMMABLE).PGPB,USPT.	17

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